Instructor: Dr. Özgür Aktaş
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Office hours: Thursday 1:30 to 3:30 PM

Assistants: Emre Alptekin, Atilla Özgüç Çakmak

Text: Digital Integrated Circuits, J.E. Ayers

Policies:
Grading:
   HW: %10, Quiz 10%,
   Attendance/Participation %10,
   Labs %15, Midterm: %25, Final %30

HWs, reports: 100% credit to part that is on time,
               90% credit to part that is up to 1 week late
               0% after 1 week

Topics to be covered:
1. Introduction, definitions, fabrication technology
2. Semiconductor fundamentals
3. Diodes, diode logic
4. Bipolar transistors
5. Metal-oxide-semiconductor transistors
6. Tools: spice, magic (maybe)
7. RTL, DTL, TTL, ECL
8. NMOS, CMOS, BiCMOS
9. MESFET, DCFL
10. Memory structures
11. Interfacing
12. Bistable circuits and wave shaping