

# Offer for a 3-Months Internship for Bachelor Students

## Topic: IC Design Kit Development

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### Abstract:

Improving the IC design kits for the Tanner EDA Software by programming callback functions using TCL for the front end. Programming layout macros using C for creating parameterized cells of high voltage transistors, resistors, capacitors etc. for the back-end. Integrating utilities such as "Safe Operating Area Check" (SOAC) into the design kits using Verilog AMS.

### Tasks:

- TCL Scripts for callback functions
- C Programming for layout macro generation
- SOAC implementation in Verilog AMS

### Requirements:

TCL, C, Verilog AMS, IC Design Familiarity

Fraunhofer IIS will pay an appropriate allowance to cover living costs and will also provide for accommodation and medical insurance during your stay in Erlangen. Travel expenses will not be reimbursed.

### Application:

Please send your formal application including CV, a copy of your *valid* passport or ID card, motivation letter, latest grades report and the date of your earliest possible start to:

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**About the Fraunhofer IIS department “Integrated Circuits and Systems”:**

Development of mixed analog/ digital circuits is one of the core competencies of the Fraunhofer IIS. Regular Multi-Project-Wafer programs and cooperation contracts with our industrial foundries allow us to offer complete solutions comprising IC development, design services, prototyping and pilot fabrication with a proven transition to high volume production directly through our foundries.

By the end of 2005 the Project Group for Optical Communications moved to Erlangen to exploit the synergies with the IC Design activities and to widen its scientific spectrum into the area of optical sensors.

For further information please visit our website: [www.iis.fraunhofer.de](http://www.iis.fraunhofer.de)